CMOS Sensor for the Cold and Tiny

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TPC is wonderful



Signal extraction remains a challenge

Schematic of the Time Projection Chamber. Magnetic and drift electric fields are parallel to the cylinder axis and beam direction. Not all the readout-plane wires are shown. Figure 4

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TPC signal extraction



XENON, LUX, LZ, etc.

- Liquid Xenon TPC
- Time Projection Chamber
- Wire and/or light readout
- mm~cm spatial resolution



- µ-PIC (Micro Pixel Chamber)
- Printed Circuit Board technology
- 400µm pitch
- Some electron gain in gas
- Difficult for readout and scale-up



recoil

- S.E. Vahsen et al. http://arxiv.org/abs/1110.3401
- D³, InGrid etc.
- Charge multiplication stage
- Non-specific ASIC readout
 - FE-I3/-I4
 - TimePix

Readout?





$\mathsf{GEM} \Rightarrow \mathsf{FEC} \Rightarrow \mathsf{MPD} \Rightarrow \mathsf{DAQ}$



Avalanche gain?



- MicroMegas
- Micromesh placed (very) close to readout PCB

• GEM

- Array of micro-holes in thin foils with conductor cladding on both sides
- µ-PIC (Micro Pixel Chamber)
- Printed Circuit Board technology

- Catch charge as early as possible, convert to digital information immediately
- Low noise
- Full 3D information
 - Truly (massively) pixelated (2D)
 - Adequate timing (waveform digitization)
- Affordable

Topmetal CMOS direct charge sensor







Topmetal-I

Topmetal-II-

- Direct voltage readout
- High analog bandwidth

- Charge sensitive amplifier, <15e- noise
- Clock-less, frame-less logic hits readout





Topmetal-II- seeing alpha (ion) tracks in air



Electron-track Compton Imaging



http://www-cr.scphys.kyoto-u.ac.jp/research/MeV-gamma/wiki/wiki.cgi?page=Top_en

Ne(90%)+DME(10%) @ 80kPa, 55Fe events (5.9keV X-ray), GEM on Topmetal-II-







 2.5×2.5 mm field of view

CMOS charge sensor array for $0\nu\beta\beta$



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Sensor array



Topmetal-S 1mm version x19 array data

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Re-sample

Refresh

CMOS works in the cold

State-of-the-art cold electronics

77K – LArPix, Q-Pix etc.

|| | BERKELEY LAB

CMOS @ <4K

Conventional CMOS designed to work at <4K temperature. Low-noise Transimpedance Amplifier (TIA) replaces SQUID

55nm CMOS devices proven to function down to ~I0mK

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NMOS1 10~20mK NMOS1 60.8K NMOS1 298K

Interfacing quantum sensors

NTD in CUORE

TES in CUPID

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Stay Tuned!

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