

Assignment – 8

Lecture-23

(April 24th, 2025)

Q1. Use as many pragmas as possible from the list to minimize the latency and resource utilization for following code. Report the new code, latency, timing and resource utilization.

List of Pragmas

Aggregate	Inline	Unroll	Array reshape
Expression_balance	Occurrence	Dependence	Allocation
Latency	Interface	Loop_flatten	Bind_op
Performance	Stream	Loop_merge	Bind_storage
Protocol	Dataflow	Loop_tripcount	Function_instantiate
Stable	Pipeline	Array Partition	

Program to be used to implement pragmas:

<https://github.com/varuns23/TAC-HEP-FPGA/tree/main/tutorial/wk12lec23/ex-all>